

IN THE CLAIMS

Please amend the claim as follows:

1. (Currently Amended) An apparatus for converting pulse code modulation (PCM) signals from either one of two different modulation standards to the other one of said two different modulation standards in a system characterized by having a plurality of communication channels with each channel having a plurality of input digital signals modulated by said one of said two different modulation standards, said apparatus comprising:

a channel selector for generating a channel select signal for identifying one of said at least one channel of the multiple plurality of communication channels in said system;

at least one codec means for selectively converting said identified input digital signals received in said at least one one of said plurality of communication channels from said one of said two different modulation standards to the other one of said two different modulation standards in response to said channel select signal; and,

a plurality of mixers for selectively releasing and transmitting said input digital signal modulated by said one of said two different modulation standards and said converted input digital signal to the other one of said two different modulation standards by said codec means in response to said channel select signal.

2. (Currently Amended) The apparatus as set forth in claim 1, wherein said codec means further comprises:

a first sub-codec means for converting said input digital signal modulated by said one of two different modulation standards received in said identified at least one channel of the multiple channels into an analog signal in response to said channel select signal, and

a second sub-codec means for converting said converted analog signal by said first codec means into corresponding digital signals in accordance with the other of said two different modulation standards in response to said channel select signal.

3. (Original) The apparatus as set forth in claim 1, wherein said mixer further comprises a first buffer for receiving said converted input digital signal by said other one of said two different modulation standards from said codec means, and a second buffer sharing an output terminal with said first buffer for receiving said input digital signal modulated by said one of said two different modulation standards.

4. (Original) The apparatus as set forth in claim 1, wherein said channel select signal is generated in response to a frame sync signal, a clock signal, and a read address controlled by said clock signal for reading an output data.

5. (Original) The apparatus as set forth in claim 4, wherein said channel select signal is synchronized with said at least one channel of the multiple channels containing said input digital signals modulated by said one of said two different modulation standards.

6. (Currently Amended) An apparatus for converting pulse code modulation (PCM) signals from either one of two different modulation standards to the other one of said two different modulation standards in a system characterized by having a plurality of communication channels with each channel having a plurality of input digital signals modulated by said one of said two different modulation standards, said apparatus comprising:

a channel selector for generating a channel select signal for identifying one of said at least one channel of the multiple plurality of communication channels containing said input digital signals modulated by said one of said two different modulation standards;

a first codec means for selectively converting said input digital signal modulated by said one of two different modulation standards and received in said identified one of said at least one channel of the multiple plurality of communication channels into an analog signal in response to said channel select signal;

a second codec means for converting said converted analog signal by said first codec means into corresponding digital signals in accordance with the other of said two different modulation standards in response to said channel select signal; and,

a plurality of mixers for selectively releasing and transmitting said input digital signal modulated by said one of said two different modulation standards and said converted input digital signal to the other one of said two different modulation standards by said codec means in response to said channel select signal.

7. (Original) The apparatus as set forth in claim 6, wherein said mixer further comprises
a first mixer for selectively releasing and transmitting said input digital signal modulated by said one of said two different modulation standards and said converted input digital signal by the other of said two different modulation standards by said second codec means in response to said channel select signal, and

a second mixer for selectively releasing and transmitting said input digital signal modulated by said one of said two different modulation standards and said converted input digital signal by the other of said two different modulation standards by said first codec means in response to said channel select signal.

8. (Original) The apparatus as set forth in claim 7, wherein said first mixer further comprises

a first buffer for receiving said converted input digital signal by the other of said two different modulation standards by said second codec means, and a second buffer sharing an output terminal with said first buffer for receiving said input digital signal modulated by said one of said two different modulation standards.

9. (Original) The apparatus as set forth in claim 7, wherein said second mixer further comprises:

a third buffer for receiving said converted input digital signal by the other of said two different modulation standards by said first codec means, and a fourth buffer sharing an output terminal with said first buffer for receiving said input digital signal modulated by said one of said two different modulation standards.

10. (Original) The apparatus as set forth in claim 6, wherein said channel select signal is generated in response to a frame sync signal, a clock signal, and a read address controlled by said clock signal for reading an output data.

11. (Original) The apparatus as set forth in claim 10, wherein said channel select signal is synchronized with said at least one channel of the multiple channels containing said input digital signals modulated by said one of said two different modulation standards.